

03/14/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

24	O.I.P.E.	hm	PATENT DATE
SCANNED	KC4	Q.A.	TC

APPLICATION NO. 09/808306	CONT/PRIOR D F	CLASS <u>712</u>	SUBCLASS <u>24</u>	ART UNIT <u>2124</u> 2813 <u>2813</u>	EXAMINER'S <u>Shnyder</u> <u>Chen, Teddie</u>
NTS Kenichi Kawaguchi		717	151	2124	<u>2813</u>

Processor for making more efficient use of idling components and
program conversion apparatus for the same

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL					CROSS REFERENCE(S)								
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION													

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.		CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Assistant Examiner) (Date)		ISSUE FEE Amount Due Date Paid	
	_____ (Primary Examiner) (Date)		ISSUE BATCH NUMBER	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			

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